AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning at page 1, line 18, as follows:

One increasingly popular application for wireless systems are wireless local area networks (WLANs) of computer systems. Prominent in the field of home and business, wireless computer networks include the wireless standards known as 802.11. The first standard to be available in commercial products was 802.11b. It supports data rates from 1 Mb/s to 11 Mb/s. To ensure data integrity at such data rates, the for communication encompasses many considerations, including equalization of distortion.

Please amend the paragraph beginning at page 1, line 24, as follows:

Decision feedback equalizations (DFE) are known to be powerful methods to equalize a distortion caused by multi-path channels, and still have rather small implementation complexities. However, if the communication system utilizes error correction codes, a DFE is not possible to implement due to a delay required for decoding of the error correction code, which makes the decision for the feedback no longer available for the equalization of a next symbol.

Please amend the paragraph beginning at page 4, line 9, as follows:

Referring now to Figure 1, the system includes a decision feedback (DF) equalizer 10 after a feed forward equalizer (FFEQ) 12, the FFEQ 12 receiving data at 22 MHz, and after a feedback filter (FBF) 14 subtraction by subtraction logic 16.

Modified CCK selector 18 is coupled to the output of the DF 10 and includes an 'iciWeight' based code word estimation, as described further hereinbelow. A tap calculator 20 is coupled to receive data from an error calculator (Err calc) 22, the error calculator 22 coupled to the output of the subtraction logic 16 and to an output of the modified CCK selector 18. ~~Tha~~The tap calculator 20 is also coupled to receive data from a loop filter 24 and an output tap data to FBF 14, the FBF 14 also coupled to modified CCK selector 18. The modified CCK selector 18 is further coupled to phase logic 26 which sends phase data to the loop filter 24, the loop filter 24 coupled to Phase Accumulator 28. Phase Accumulator 28 also receives offset frequency (FrOffset) data and passes data to the FFEQ 12. The system outputs code bits from the modified CCK selector 18 through decision (Dec) logic 30.

Please amend the Abstract at page 13 as follows:

Aspects for equalization in a communications system are described. The aspects include utilizing a block code based error correction scheme in a modulation system of the communication system, and removing cursor inter-symbol interference within an error ~~code~~ correction code word to make code word decision with minimum error power-based criteria in the block code based error correction scheme.